

Figure 1

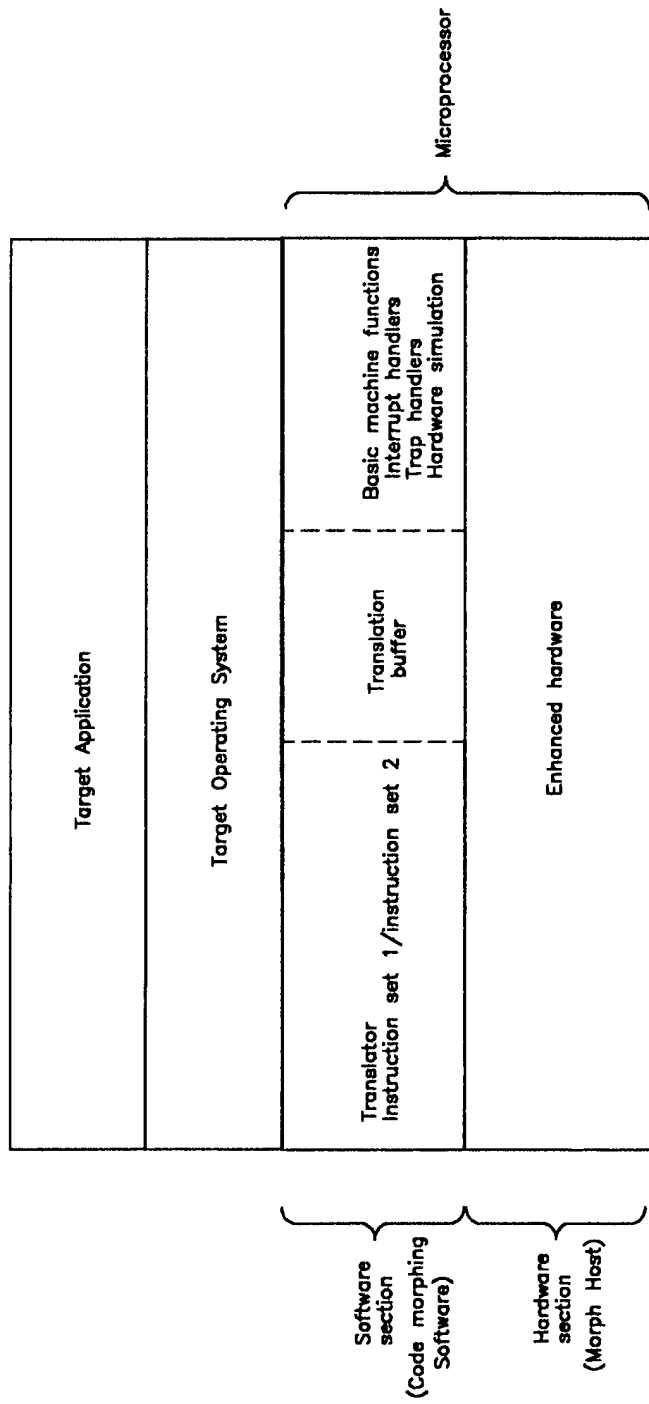


Figure 2

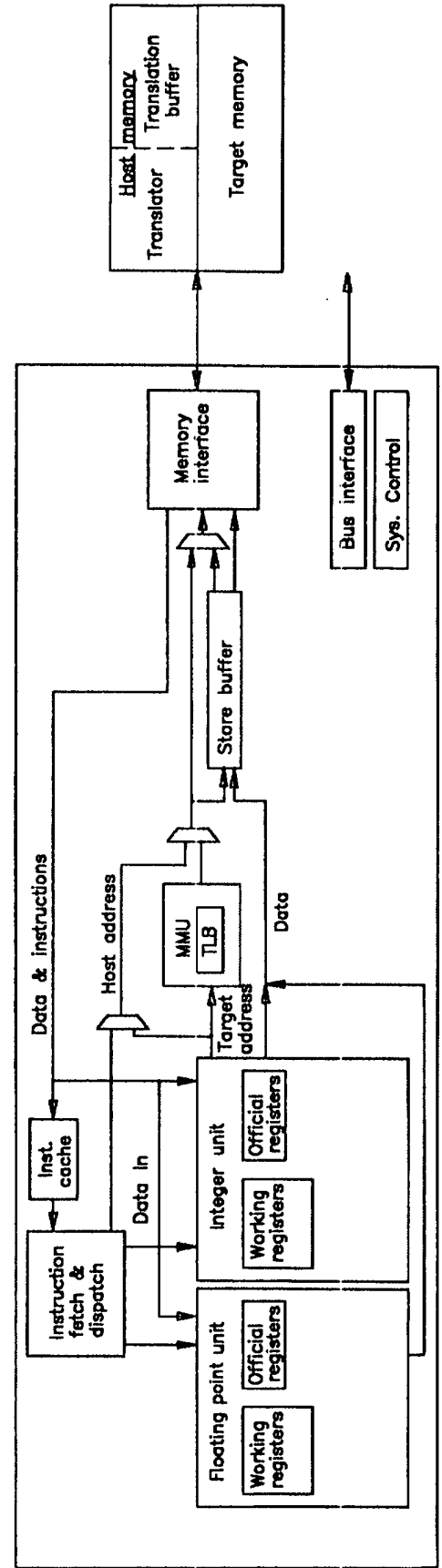
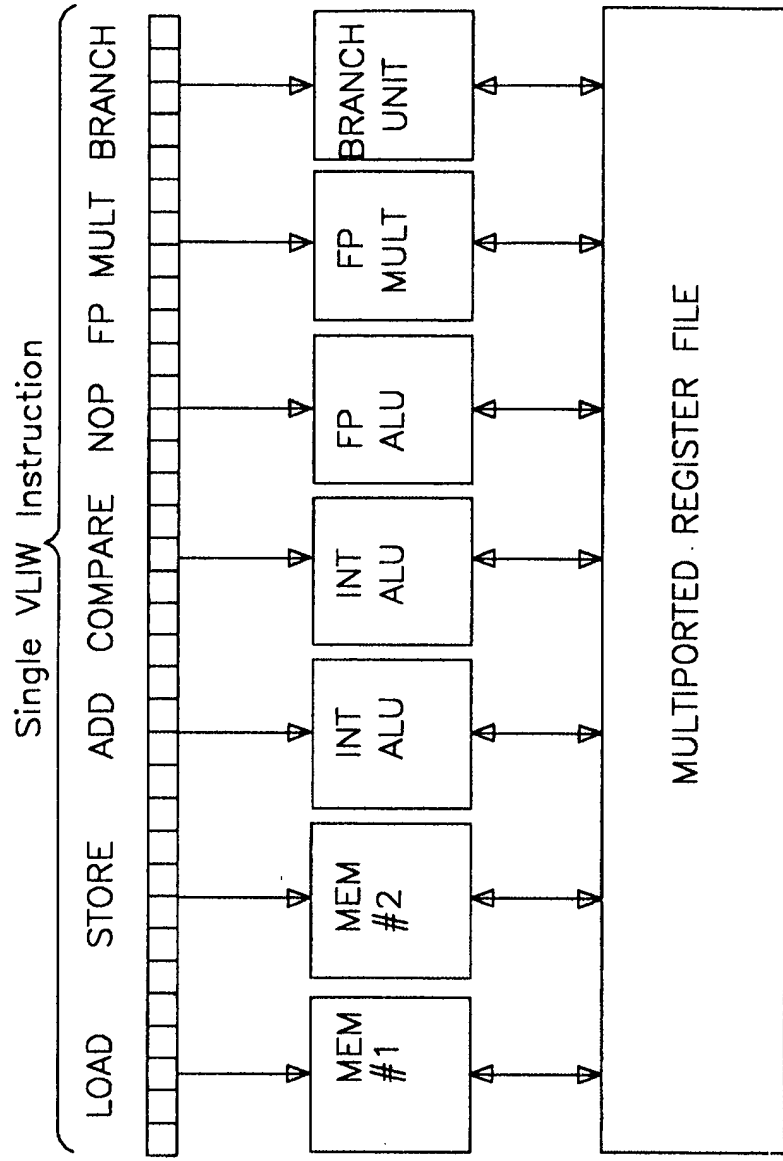
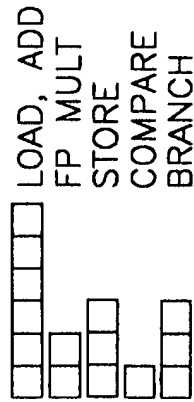


Figure 3

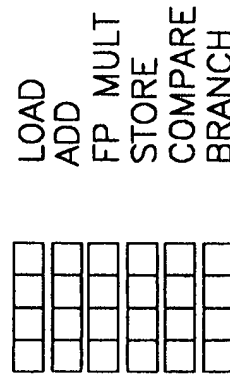


(c)

Figure 6



(a)



(b)

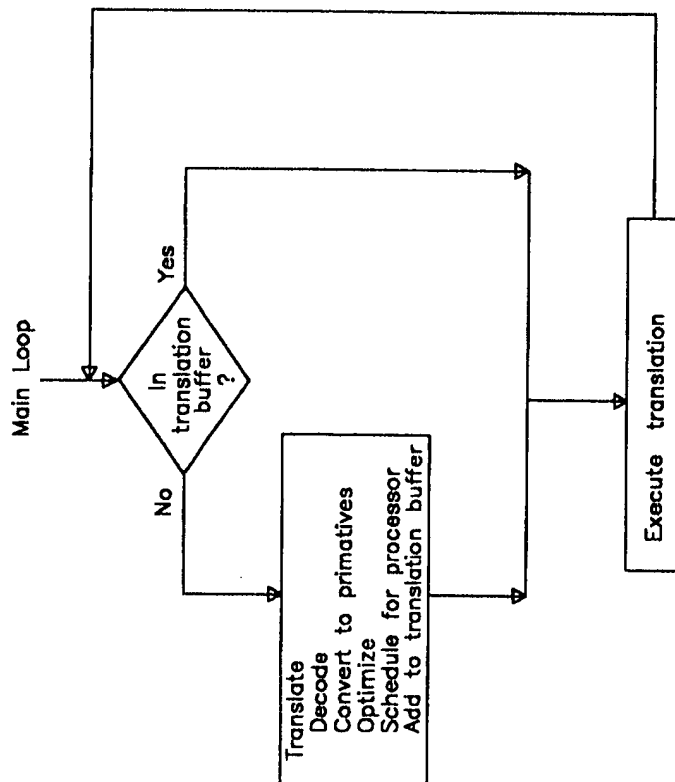


Figure 7

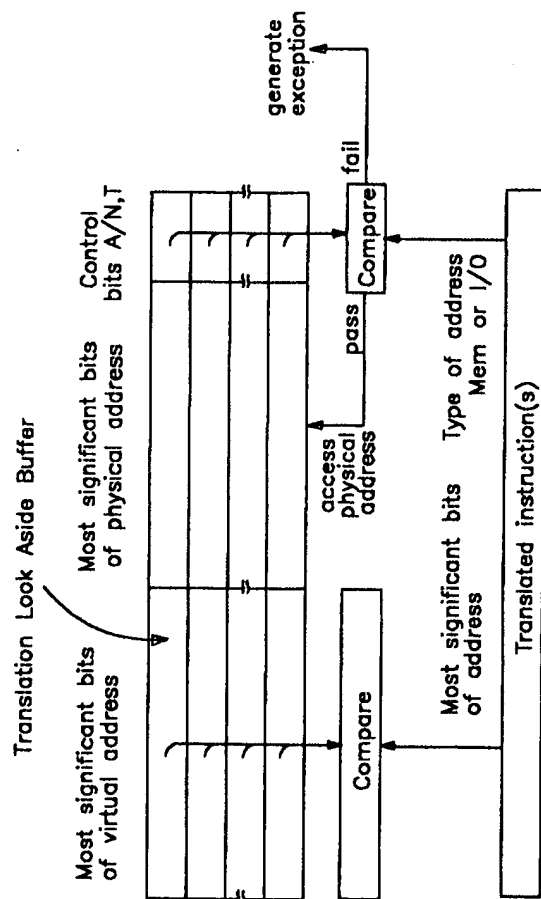


Figure 11

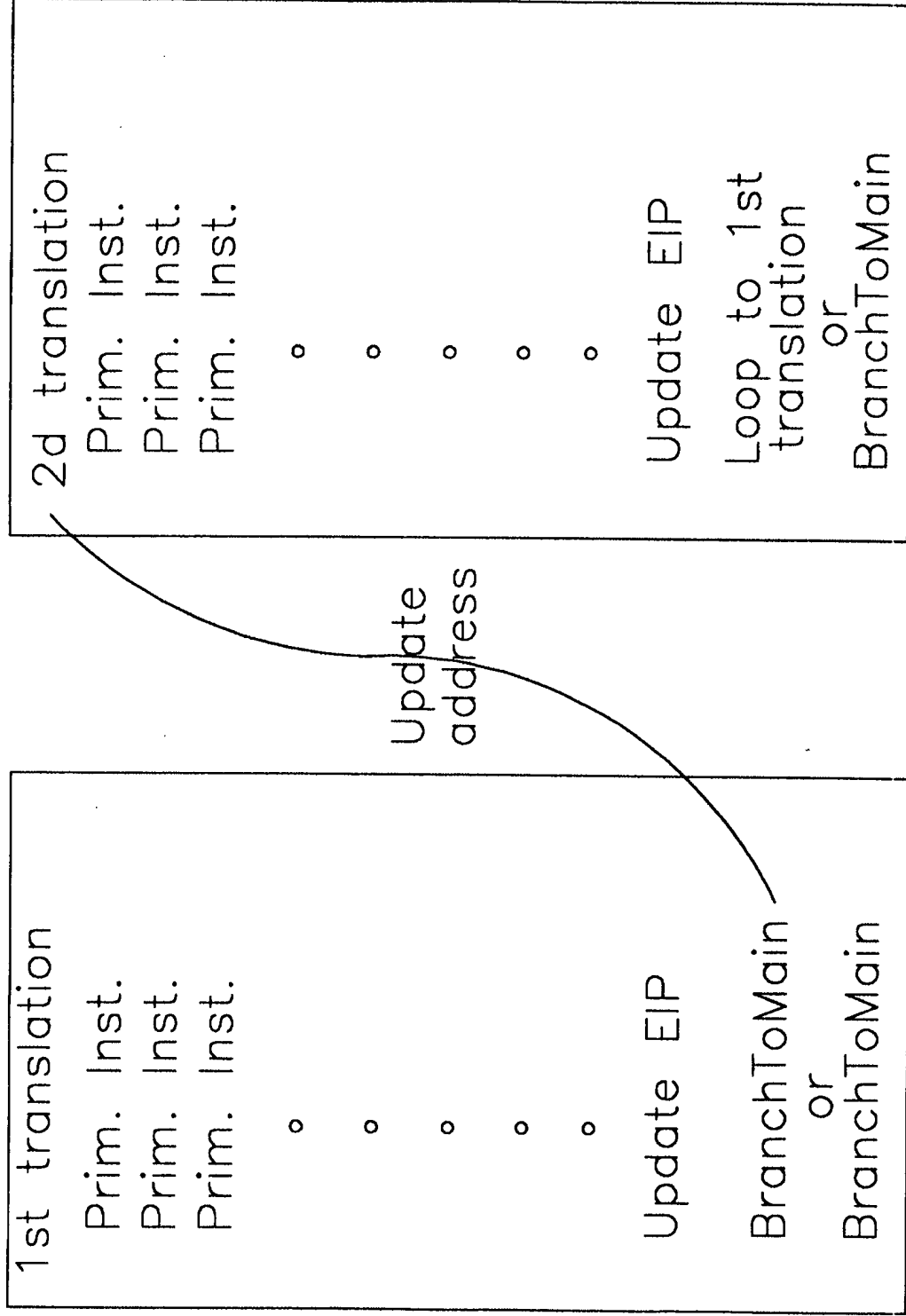


Figure 8

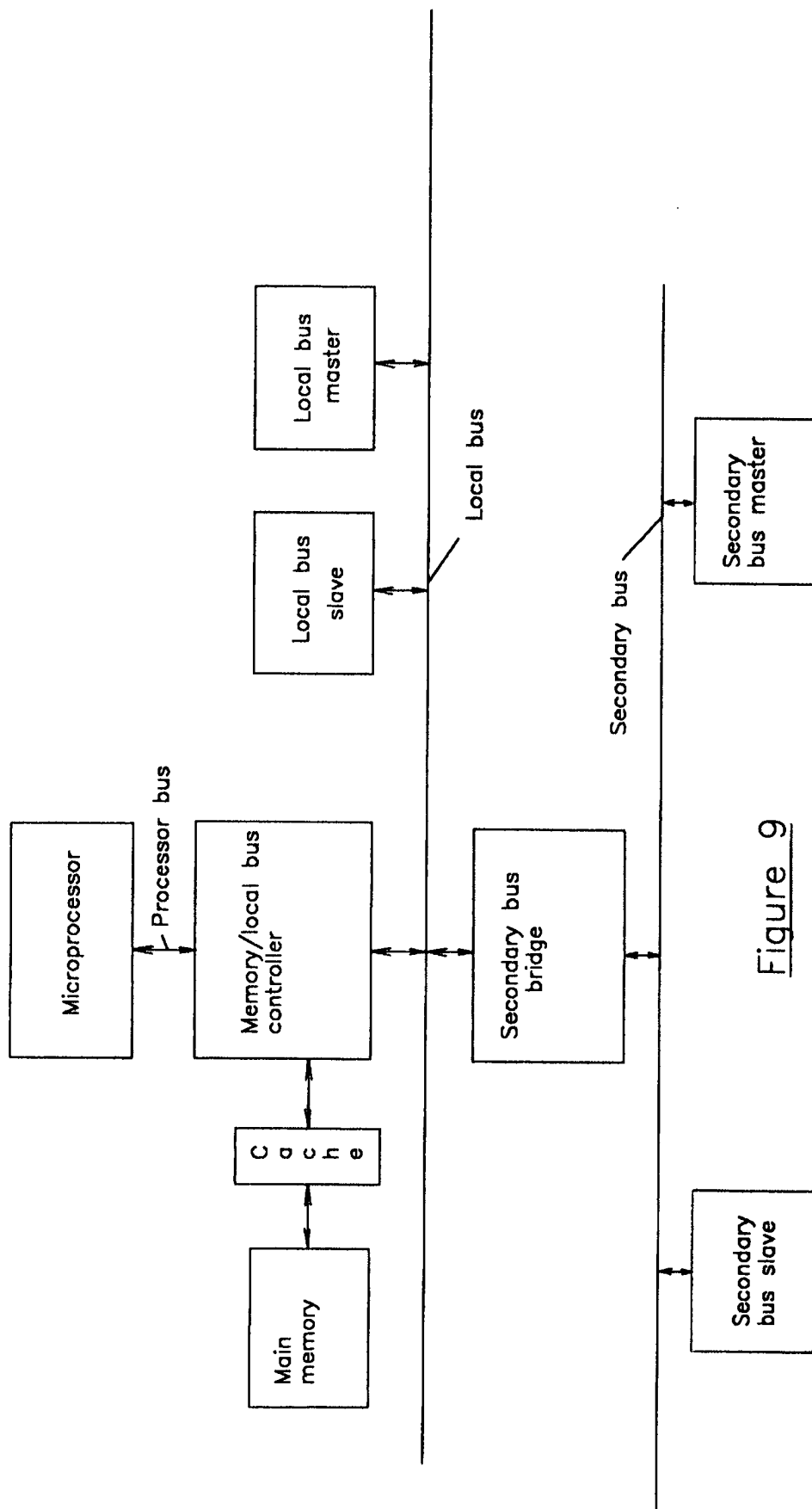


Figure 9

Mark code at memory address as translated
by marking physical target address in TLB

Write to memory address

Detect mark indicating translated code

Protect memory address holding translated code
by generating exception & invalidating code translation

Figure 12